

IDS #1

09/913453

Information Disclosure Statement  
 August 14, 2001  
 Page 3

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Form PTO-1449 (Modified) Page 1 of 1	ATTY DOCKET NO. B-4276PCT 619003-1	U.S. SERIAL NO. Not yet assigned
<b>LIST OF PATENTS AND PUBLICATIONS STATEMENT</b>	INTERNATIONAL APPLICATION NO. PCT/GB00/00504	INTERNATIONAL FILING DATE 15 February 2000
	APPLICANT(S) Graeme John PROUDLER, et al.	
	FILING DATE 8/14/01 Concurrently herewith	GROUP 2661 Not yet assigned

**U.S. PATENT DOCUMENTS**

Examiner Initial		Document Number	ISSUE Date	Name	Class	Subclass	Filing Date if Appropriate
TP	1	5,444,850	08/22/95	Chang	395	200.1	08/04/93
TP	2	5,680,547	10/21/97	Chang	395	200.1	08/08/95

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO
TP	3	WO 98/25372	06/11/98	WO	—	—	Y
TP	4	WO 97/37305	10/09/97	WO	—	—	Y
TP	5	EP 0 849 657 A1	06/24/98	EP	—	—	Y
TP	6	WO 97/07463	02/27/97	WO	—	—	Y
TP	7	WO 95/24696	09/14/95	WO	—	—	Y
TP	8	EP 0 848 315 A2	06/17/98	EP	—	—	Y
TP	9	WO 98/15082	04/09/98	WO	—	—	Y
TP	10	EP 0 510 244 A1	10/28/92	EP	—	—	Y

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

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EXAMINER	DATE CONSIDERED
	6/23/05

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LIST OF PATENTS AND PUBLICATIONS STATEMENT	APPLICANTS Graeme John Proudler, et al.	
	FILING DATE August 14, 2001	GROUP 2151 2661

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE or 102(e) DATE IF APPROPRIATE
TP	1 5,421,006	5/95	Jablon et al.	395	575	—
TP	2 6,330,670 B1	12/01	England et al.	713	2	1/8/99

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO
TP 3	94/01821	7/93	WO	—	—	Y
TP 4	98/36517	8/98	WO	—	—	Y

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

TP 5	Anderson, R. and Markus Kuhn, "Tamper Resistance - a Cautionary Note," 16 pages, located at Internet address <www.cl.cam.ac.uk/~mgk25/tamper.html> (1996).
TP 6	Berger, J.L., et al., "Compartmented Mode Workstation: Prototype Highlights," IEEE Transactions on Software Engineering, Vol. 16, No. 6, pp. 608-618 (June 1990).
TP 7	Intel, "Wired for Management Baseline specification v2.0," Boot Integrity Services Application Programming Interface Version 1.0, 64 pages (December 28, 1998).
TP 8	"Information technology-Security techniques-Entity Authentication - Part 3: Mechanisms using digital signature techniques," ISO/IEC 9798-3, 6 pages (1998).
TP 9	"Information technology-Security techniques-Key management - Part 3: Mechanisms using asymmetric techniques," ISO/IEC 11770-3, pp. 1-23 and Annexes A-E (1999).
TP 10	The Trusted Computing Platform Alliance, "Building a Foundation of Trust in the PC," 9 pages, located at Internet address <www.trustedpc.org/home/home.html> (January 2000).

IDS#2

Information Disclosure Statement  
 USSN 09/913,453  
 April 15, 2002  
 Page 4



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TP	11	Trusted Computing Platform Alliance, Main Specification Version 1.0, 284 pages (January 25, 2001).
TP	12	Wiseman, S., et al, "The Trusted Path between SMITE and the User," <i>IEEE Proceedings of the Symposium on Security and Privacy</i> , pp. 147-155 (1988).

EXAMINER	DATE CONSIDERED
<u>Wolan</u>	6/23/05

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LIST OF PATENTS AND PUBLICATIONS STATEMENT	APPLICANTS Graeme John Proudler, et al.	
	FILING DATE August 14, 2001	GROUP 2151 2661

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE or 102(e) DATE IF APPROPRIATE
TP	5,361,359	11/94	Tajalli et al.	395	700	—

## FOREIGN PATENT DOCUMENTS

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 Page 3



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LIST OF PATENTS AND PUBLICATIONS STATEMENT	INTERNATIONAL APPLICATION NO. PCT/GB00/00504	INTERNATIONAL FILING DATE 15 February 2000
	APPLICANT(S) Graeme John Proudler, et al.	
	FILING DATE August 14, 2001	GROUP 2151-2681

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE OR 102(e) DATE IF APPROPRIATE
TP	6,138,239	10/2000	Veil	713	200	

## FOREIGN PATENT DOCUMENTS

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TP	94/01821	1/1994	WO	—	—	Y

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TP	Yee, B., "Using Secure Coprocessors," Doctoral thesis - Carnegie Mellon University, pp 1-94 (May 1994).
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<i>Urban</i>	6/23/05

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